



MINISTRY OF EDUCATION AND CULTURE
UNIVERSITAS NEGERI SURABAYA
FACULTY OF MATHEMATICS AND NATURAL SCIENCES
DEPARTMENT OF PHYSICS

Ketintang Campus, Jalan Ketintang, C3 Building, Surabaya 60231
 Website: <https://pendidikan-fisika.fmipa.unesa.ac.id/>, email: s1-pfis@unesa.ac.id

Undergraduate Programme of Physics Education

Module Handbook

Module Name :	<i>Elektronika Dasar II</i> Basic Electronics II
Module level :	Bachelor degree/Undergraduate Programme
Course Code :	8420302241
Abbreviation, if applicable:	-
Courses included in the module, if applicable:	Not Applicable
Semester/Term	4/Second Year
Module coordinator(s)	Drs. Imam Sucahyo, M.Si.
Lecturer(s):	Drs. Imam Sucahyo, M.Si. Endah Rahmawati, S.T., M.Si. Abd. Kholiq, S.Pd. M.T. Dzulkiflih, S.Si., M.T. Meta Yantidewi, M.Si.
Language:	<i>Bahasa Indonesia</i>
Classification within the curriculum:	Compulsory/ Elective
Teaching format/class hours per week during the semester:	3 contact hours of lectures (Indonesia credit semester or sks*)
Workload :	3 x 50 minutes lectures, 3 x 60 minutes structured activity, 3 x 60 minutes individual activity, 14 weeks per semester, 135 total hours per semester ~ 4.77 ECTS**
Credit Point:	3 sks (4.77 ECTS)
Requirements:	Basic Electronics I
Learning goals/competencies:	<ol style="list-style-type: none"> 1. Analyzing work principles of transistor amplifier BJT (Grounded Emitter Transistor) using some relevant examples 2. Analyzing work principles of field-effect transistor (JFET) by giving some relevant examples and JFET amplifier 3. Analyzing work principles of operational amplifier (Op-Amp) using some relevant examples 4. Explaining the basics of digital electronics by giving some relevant examples 5. Applying (principles concepts of basic electronics 2) according to sciences and technologies development and relevant to competency demands in national educations standards
Content	The Basic Electronics 2 course contain work principles and applications of BJT transistor as power amplifier, basic principles of field-effect transistor JFET amplifier, characteristics of Op-Amp



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	circuits, oscillator circuits and basic materials of digital electronics.										
Attribute Soft skill:	Scientific report, public speaking, and team work										
Study/exam achievements:	Students are considered to complete the course and pass if they obtain at least 40% of maximum final grade. The final grade (NA) is calculated based on the following ratio:										
	<table border="1" style="width: 100%;"> <thead> <tr> <th style="text-align: left;">Assessment Components</th> <th style="text-align: left;">Percentage of contribution</th> </tr> </thead> <tbody> <tr> <td>Participation</td> <td>20%</td> </tr> <tr> <td>Assignment</td> <td>30%</td> </tr> <tr> <td>Mid-semester test</td> <td>20%</td> </tr> <tr> <td>Final semester test</td> <td>30%</td> </tr> </tbody> </table>	Assessment Components	Percentage of contribution	Participation	20%	Assignment	30%	Mid-semester test	20%	Final semester test	30%
	Assessment Components	Percentage of contribution									
	Participation	20%									
	Assignment	30%									
Mid-semester test	20%										
Final semester test	30%										
Learning Methods :	Student-centered approach, lecture and discussion, and presentations (structured activities)										
Form of Media:	<i>Power Point</i> slides, e-book file, and multimedia.										
Literature (primary references):	<ol style="list-style-type: none"> 1. Sutrisno. 1978. Elektronika 2. Teori dan Penerapannya. Penerbit ITB Bandung. 2. Rahmawati, E., Sucahyo, I., dan Kholiq, A. 2017. Hand out Elektronika Dasar 2 3. Rahmawati, E., Sucahyo, I., dan Kholiq, A. 2017. Panduan Praktikum Elektronika Dasar 2 4. Tooley, M. 2006. Electronics Circuit: Fundamentals and Applications. Third Edition. Elsevier Ltd. 5. Boylestad, R., and Nashelsky, L. Electronics Devices and Circuits: Theory. Seventh Edition. Prentice Hall. 6. Floyd, T. L. 2012. Electronics Devices. Prentice Hall. 7. Tooley, M. 2006. Electronics Circuit: Fundamentals and Applications. Third Edition. Elsevier Ltd. 8. Boylestad, R., and Nashelsky, L. Electronics Devices and Circuits: 9. Theory. Seventh Edition. Prentice Hall. 10. Floyd, T. L. 2012. Electronics Devices. Prentice Hall. 										
Notes:	*1 sks in learning process = three periods consist of: (a) scheduled instruction in a classroom or laboratory (50 minutes); (b) structured activity (60 minutes); and (c) individual activity (60 minutes) according to the Regulation of Indonesia Ministry of Research, Technology, and Higher Education No. 44 Year 2015 jo. the Regulation of Indonesia Ministry of Research, Technology, and Higher Education No. 50 Year 2018.										
	**1 sks = 1,59 ECTS according to Rector Decree Of Universitas Negeri Surabaya No. 598/Un38/Hk/Ak/2019										